

OEM 16x64



product line

This multiplexed architecture 16x64 provides an efficient and cost-effective solution to Original Equipment Manufacturers for integration into third-party systems. The board and dynamic-link libraries (dlls) are available.

CONFIGURATIONS : 16x64



pulsers	Adjustable voltage: 20 to 80V with 1V step Negative rectangular pulse, adjustable width: 30 ns to 625 ns, step of 2.5 ns Rise time < 10 ns (80V, 50 Ω) Max. PRF: 30 KHz
receivers	Bandwidth: 0.8 to 20MHz, adjustable gain on each channel from 0 to 80 dB Adjustable analog DAC on 80 dB (max. 40 dB/μs) synchronized on events Cross-talk between two channels > 50 dB, max. input signal amplitude: 0.8 Vpp
digitizer	Max. sampling frequency: 100 MHz (adjustable from 100 MHz to 6.6 MHz) Range : 10 bits Input impedance: 50 Ω Global delay: 0 up to 1.6 ms, step of 10 ns Delay-laws at transmission/reception: 0 to 20 μs, step of 2.5 ns Digitizing depth: up to 50,000 samples (8,000 samples max. per elementary channel) FIR filters
embedded processors	FPGA on CPU-board
hardware configuration	Multiplexed architecture: 16x64
libraries	dll provided
dimensions	L x W x H: 210mmx 100mmx 50mm- Weight: ~500g
I-O	1 Hypertronix connectors, 3 encoders input, 1 external trigger 1 USB2, 2 LEMO connectors (type 00) External power supply input