OEM
32x32 64x64

product line

With up to 64 parallel channels, these fully parallel architecture boards offer high performance at an affordable cost to Original Equipment Manufacturers for integration into third-party systems. Both boards and dynamic-link libraries (dlls) are available.

CONFIGURATIONS : 32x32, 64x64

pulsers	Adjustable voltage: 30 to 100V with 1V step, negative rectangular pulse Adjustable width: 30 ns to 625 ns, step of 2.5 ns, rise time < 10 ns (200V, 50 Ω) max. PRF: 30 KHz
receivers	Bandwidth: 0.8 to 20MHz, adjustable gain on each channel from 0 to 80 dB Adjustable analog DAC on 80 dB (max. 40 dB/µs) synchronized on events Cross-talk between two channels > 50 dB, max. input signal amplitude: 0.8 Vpp
digitizer	Range: 10 bits FIR filters Max. sampling frequency: 100 MHz (interleaved) Input impedance: 50 Ω Global delay: 0 up to 1.6 ms, step of 10 ns Delay-laws at transmission/reception: 0 to 20 µs, step of 2.5 ns Digitizing depth: up to 50,000 samples (4,000 samples max. per elementary channel)
embedded processors	FPGA on CPU-board
libraries	dll provided
hardware configuration	Parallel architecture: 32- and 64-channel
platform	Windows-based PC, USB2 link between Hardware and PC (desktop or laptop)
dimensions	L x W x H: 200mm x 160mm x 64mm - Weight: ~800g
I-O	1 Hypertronix connector, 3 encoders input, 1 external trigger 1 USB2, external power supply input

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